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To Whom it May Concern

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Recommendation for Mr. M.Sc. Michael Geiger

I have great pleasure in writing this letter of recommendation for my former student Mr. M.Sc. Michael Geiger. I know him from 2010 – 2015 as an undergraduate and MS student in Electrical Engineering and Information Technology at the Karlsruhe Institute of Technology, where I taught the design methods of electronic systems and microsystems, and directed both his Bachelor as well as his Master's thesis.

He took many courses with me including Introduction to Information Technology, Programming in C and C++, Software Engineering, Communication Systems and Protocols, Hardware/Software Codesign, and Digital System Design. In this long association, I had ample opportunity to evaluate his performance and came to the conclusion that he has an excellent ability to grasp complicated concepts and apply them in tackling complex problems.

Mr. Geiger is very motivated, eager to enhance his knowledge, and is always passionate about learning. He ranked in top 5% in my lecture Hardware/Software Co-design and among the best 20% of all students in electrical engineering and information technology at Karlsruhe Institute of Technology - KIT. He is continuously very attentive in class, always interested in new ideas and highly articulate in his presentation. A excellent example of this was his final presentation of his master thesis on developing and implementing successfully innovative concepts in accelerating software loops in hardware, based on new architectural processor solutions called i-core within the large german special research initiative InvasIC. In addition he did a very good seminar work in medical imaging development and a highly appreciated internship project at Xilinx Research Lab in Dublin/Ireland. All his presentations made it clear that not only his technical knowledge was excellent, but also his communication of the subject matter was flawless. Moreover, he has a great enthusiasm for pursuing higher academics, particularly in furthering his education and research in the field of embedded hardware and software design.

During his thesis research, he has proven that he is extremely determined to find new ways of overcoming difficulties. He understands very well that research can be challenging in that it may be cumbersome to reach meaningful results. Yet he also understands that the effort put in research can be very rewarding when the outcome is successful. Michael Geiger's main specialization was in

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the field of systems engineering and embedded systems and he quickly became one of the top students in his class.

His current zeal lies in embedded hardware and software design as well as successful implementation, an area in which he has tremendous potential to contribute. I believe there will be very promising opportunities for us to work on joint research and development projects with truly fruitful outcomes in the future.

Mr. Geiger has in addition an excellent professional, industrial and general soft skill student background. First, he worked at Xilinx Research Labs in Dublin, the world-wide leading FPGA company, where he successfully developed his expertise and applied it in the specification, design and synthesis of complex hardware systems. His additional various presentation, sports, team and international experiences broadened much his very positive personality as well as leading and communication skills, demonstrating innovation, creativity and impressive performance.

In a nutshell, Mr. M.Sc. Michael Geiger is a very talented young engineer with enormous potential to make important contributions to the academic and industry in embedded hadware/software systems. His intelligence combined with his commitment and compassionate personality will certainly make him an appreciated member of each team, institution and company. I strongly encourage you to look favorably upon his application and can recommend him without any limitation.

Yours sincerely,

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Prof. Dr.-Ing. Dr. h. c. Jürgen Becker